

☐ Search Results

[BROWSE](#)

[SEARCH](#)

[IEEE XPLORE GUIDE](#)

[SUPPORT](#)

Results for "((performance analysis<and> (co-simulation<in>metadata)) <and> (pyr >= 1951..."

Your search matched 8 of 1332769 documents.

A maximum of 250 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

e-mail printer friendly

» Search Options

[View Session History](#)

[New Search](#)

Modify Search

((performance analysis<and> (co-simulation<in>metadata)) <and> (pyr >= 1951 < > [Search](#) >

☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

IEEE JNL IEEE Journal or Magazine
IEEE JNL IEE Journal or Magazine
IEEE CNF IEEE Conference Proceeding
IEE CNF IEE Conference Proceeding
IEEE STD IEEE Standard

[view selected items](#) [Select All](#) [Deselect All](#)

- ☐ 1. **Performance analysis of systems with multi-channel communication architectures**
Lahiri, K.; Raghunathan, A.; Dey, S.;
[VLSI Design, 2000. Thirteenth International Conference on](#)
3-7 Jan. 2000 Page(s):530 - 537
Digital Object Identifier 10.1109/ICVD.2000.812662
[AbstractPlus](#) | Full Text: [PDF\(148 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 2. **Fast performance analysis of bus-based system-on-chip communication architectures**
Lahiri, K.; Raghunathan, A.; Dey, S.;
[Computer-Aided Design, 1999. Digest of Technical Papers. 1999 IEEE/ACM International Conference on](#)
7-11 Nov. 1999 Page(s):566 - 572
Digital Object Identifier 10.1109/ICCAD.1999.810712
[AbstractPlus](#) | Full Text: [PDF\(712 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **A methodology for architecture exploration of heterogeneous signal processing systems**
Lieverse, P.; van der Wolf, P.; Deprettere, E.; Vissers, K.;
[Signal Processing Systems, 1999. SiPS 99. 1999 IEEE Workshop on](#)
20-22 Oct. 1999 Page(s):181 - 190
Digital Object Identifier 10.1109/SIPS.1999.822323
[AbstractPlus](#) | Full Text: [PDF\(588 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **A compilation-based software estimation scheme for hardware/software co-simulation**
Lajolo, M.; Lazarescu, M.; Sangiovanni-Vincentelli, A.;
[Hardware/Software Codesign, 1999. \(CODES '99\) Proceedings of the Seventh International Workshop on](#)
3-5 May 1999 Page(s):85 - 89
Digital Object Identifier 10.1109/HSC.1999.777398
[AbstractPlus](#) | Full Text: [PDF\(348 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **Fast hardware-software co-simulation using VHDL models**
Tabbara, B.; Sgroi, M.; Sangiovanni-Vincentelli, A.; Filippi, E.; Lavagno, L.;
[Design, Automation and Test in Europe Conference and Exhibition 1999. Proceedings](#)
9-12 March 1999 Page(s):309 - 316
Digital Object Identifier 10.1109/DATE.1999.761139
[AbstractPlus](#) | Full Text: [PDF\(652 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 6. **Efficient power estimation techniques for HW/SW systems**
Lajolo, M.; Raghunathan, A.; Dey, S.; Lavagno, L.; Sangiovanni-Vincentelli, A.;

☐ 7. **An approach to the design of a deterministic jitter-free dual-chamber brady pacer in an ICD using hardware and software cooperation**

Marcotte, J.; Dropps, F.; Hsung, J.C.; Johnson, S.;
Computers in Cardiology 1998

13-16 Sept. 1998 Page(s):549 - 552

Digital Object Identifier 10.1109/CIC.1998.731924

AbstractPlus | Full Text: PDF(328 KB) IEEE CNF
Rights and Permissions

☐ 8. **AKKA: a tool-kit for cosynthesis and prototyping**

Tammemae, K.; O'Nils, M.; Jantsch, A.; Hemani, A.;

Hardware-Software Cosynthesis for Reconfigurable Systems (Digest No: 1996/036), IEE Colloquium on

22 Feb. 1996 Page(s):8/1 - 8/8

AbstractPlus | Full Text: PDF(536 KB) IEE CNF